

MALLA REDDY ENGINEERING COLLEGE (AUTONOMOUS)

(An UGC Autonomous Institution, Affiliated to JNTUH, Hyderabad Accredited 2nd time by NAAC with 'A' Grade & NBA and Recipient of World Bank Assistance under TEQIP--II S.C. 1.1)

Maisammaguda (H), Medchal-Malkajgiri District, Secunderabad,

Telangana State — 500100

www.mrec.ac.in

Department of Electronics and Communication Engineering

CIRCULAR

Date: 12/02/2016

All the students are hereby informed that Value Added Course on "Mixed Signal Design & Verification Using Cadence Tools" on 24thto 28thFebruary, 2016 is being organized by the Electronics and Communication Engineering department. The resource person for the course is "Mr.N.MADHUSUDAN REDDY, FPGA & ASIC INFOTECH PVT LTD, HYDERABAD".

Students are advised to register their names to the programme coordinator "P.S.INDRANI", on or before 20thFebruary 2016 and utilize this opportunity to enhance their skills by attending the program.

The detailed schedule of the programme will be displayed in the notice board.

Head of the Department

Copy to:

- 1) Circulation in Students classroom
- 2) All HOD's
- 3) Notice Boards
- 4) PA to principal for filing.

(Autonomous)
Maisammaguda, Dhulapally,
(Post Via Kompally), Sec'bad-500 100



Target Group:

This FDP is open to all the teachers of Electronics and Communication Engineering who have interest in learning, teaching and research on the Mixed Signal Design and verification and also Research Scholars and Industry personnel working in Design and Verification, can also register for this programme.

Registration:

Candidates can register by sending the Registration form attached in this brochure along with a **Registration fee of**Rs 300 in the form of DD drawn in favor of "Principal,
MREC (Autonomous)" payable at Hyderabad.

Transportation:

Participants can avail the Institute bus facility from different locations of the twin cities

Important Date:

Last Date for submission of Registration form by post/email: 22_{nd} February 2015.

Registration form can be downloaded from:

www.mrec.ac.in

Address for Correspondence

P.S.INDRANI

Assoc. Professor, ECE Department

Malla Reddy Engineering College (Autonomous)

Maisammaguda, Dhulapally Post,

Via Kompally, Secunderabad-500100

Email: indu.sri@mrec.ac.in

Mobile: 9490224344

Organizing Committee

Chief Patron:

Sri Ch. Malla Reddy, MP

Founder Chairman, CMR Educational Society

Patrons:

Sri Ch. Mahender Reddy Secretary, CMR Educational Society Dr. Ch. Bhadra Reddy Secretary, MRIMS

Co-Patrons:

Dr. S. Sudhakara Reddy, Principal, MREC (A) **Mr.P.Santhosh Reddy**, Director, MREC(A)

Convener:

Dr. M. J. C. Prasad, Professor & Head, ECE Dept.

Co-Ordinators:

P. S. Indrani, Assoc. Professor, ECE Dept.

M. Kavicharan, Assoc. Professor, ECE Dept

Members:

Dr. Kesavan Gopal, Professor, ECE

Mr. P. Ashok Babu, Assoc Professor, ECE

Mrs C. Silpa, Assoc Professor, ECE

Mr.B.Srinivas, Assoc Professor, ECE

Mrs P. Sowjanya, Assoc Professor, ECE

Mrs G.Bharathi Subhashini, Assoc Professor,

ECE

Mr.CH.Bhanu Prakash, Assoc Professor, ECE

Mr.T.Srinivas Reddy, Assoc Professor, ECE

Mr.T.Surender Reddy, Assoc Professor, ECE

Mr.V.Srinivas, Asst Professor, ECE

Mixed Signal Design & Verification Using Cadence Tools

One week Workshop

24th to 28th February 2016



Coordinator
P.S.INDRANI
Assoc. Professor, ECE Department

Convener
Dr. M. J. C. PRASAD
Professor & Head, ECE Department



Organized by
Department of Electronics and
Communication Engineering (ECE)

Malla Reddy Engineering College (Autonomous)

Maisammaguda, Secunderabad – 500 100 Maisammaguda, Secunderabad – 500 Maisammaguda, Secunde

Malla Reddy Engineering College 040-23090485

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Maisam naouda, Dhulapally,

MALLA REDDY ENGINEERING COLLEGE (AUTONOMOUS)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

List of Students Registered for Value Added Course

2015-16

					DATE OF	
			Year/		INTRODUCTI	DATE OF
S.No Branch	Roll Number	Student Name	Sem	Value Added Course	ON	COMPLETION
				Mixed Signal Design & Verification		
1 ECE	13J41A0403	BOYINI RAJASHEKAR	111/11	Using Cadence Tools	24-02-2016	28-02-2016
				Mixed Signal Design & Verification		
2 ECE	13J41A0416	GOLLA ANURADHA	111/11	Using Cadence Tools	24-02-2016	28-02-2016
				Mixed Signal Design & Verification		
3 ECE 13J41A	13J41A0419	JAJULA SAIRAM	111/11	Using Cadence Tools	24-02-2016	28-02-2016
	3			Mixed Signal Design & Verification		
4 ECE	13J41A0421	K MAHENDRA	111/11	Using Cadence Tools	24-02-2016	28-02-2016
			140	Mixed Signal Design & Verification		
5 ECE	13J41A0433	MOHAMMED AMIR	111/11	Using Cadence Tools	24-02-2016	28-02-2016
				Mixed Signal Design & Verification		
6 ECE	13J41A0436	SRIKANTH.ODETI	111/11	Using Cadence Tools	24-02-2016	28-02-2016
		2		Mixed Signal Design & Verification		
7 ECE	13.1-1 A0437	PADIGELA SAIKIRAN	111/11	Using Cadence Tools	24-02-2016	28-02-2016
				Mixed Signal Design & Verification		
8 ECE	13J41A0440	R SAI PRIYANKA	111/11	Using Cadence Tools	24-02-2016	28-02-2016
				Mixed Signal Design & Verification		
9 ECE	13J41A0446	SHAIK.SHARUF	111/11	Using Cadence Tools	24-02-2016	28-02-2016
				Mixed Signal Design & Verification		
10 ECE	13J41A0449	THOUTIREDDYSRINIJA	111/11	Using Cadence Tools	24-02-2016	28-02-2016
				Mixed Signal Design & Verification		
11 ECE	13J41A0452	U.PRAMODH KUMAR	111/11	Using Cadence Tools	24-02-2016	28-02-2016

Malla Reddy Engineering Colle (Autonomous) Maisammaguda, Dhulapally, (Post Via Kompally), Sec'bad-500 1

Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 28-02-2016 28-02-2016 28-02-201					Mixed Signal Design & Verification		
13 ECE	12 ECE	13J41A0454	V BHARANI REDDY	111/11	Using Cadence Tools	24-02-2016	28-02-2016
Mixed Signal Design & Verification 24-02-2016 28-02-2016 13 24 24 24 24 24 20 20 28 24 24 20 20 28 24 20 20 28 20 20 28 20 20					Mixed Signal Design & Verification		
14 ECE	13 ECE	13J41A0455	V.L.NKITHA	111/11	Using Cadence Tools	24- 02-2016	28-02-2016
Mixed Signal Design & Verification 24-02-2016 28-02-2016 28-02-2016 13J41A0469 Y GANGADHAR III/II Using Cadence Tools 24-02-2016 28-02-2016					Mixed Signal Design & Verification		
15 ECE 13J41A0459 Y GANGADHAR III/II Using Cadence Tools 24-02-2016 28-02-2016 28-02-2016 16 ECE 13J41A0460 KONDRA RAJU III/II Using Cadence Tools 24-02-2016 28-02-20	14 ECE	13J41A0457	VEERAGONI VENU	111/11		24-02-2016	28-02-2016
Mixed Signal Design & Verification 24-02-2016 28-02-2016 28-02-2016 2					Mixed Signal Design & Verification		
16 ECE 13J41A0460 KONDRA RAJU III/II Using Cadence Tools 24-02-2016 28-02-2016 2	15 ECE	13J41A0459	Y GANGADHAR	111/11	Using Cadence Tools	24-02-2016	28-02-2016
Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 28-02-2016					Mixed Signal Design & Verification		
17 ECE	16 ECE	13J41A0460	KONDRA RAJU	111/11	Using Cadence Tools	24-02-2016	28-02-2016
Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 28-02-2016 28-02-2016 28-02-201					Mixed Signal Design & Verification		
18 ECE 13J41A0461 ADI LENIN JAIDEEP III/II Using Cadence Tools 24-02-2016 28-02-	17 ECE	14J45A0411	JADALA THARUN	111/11	Using Cadence Tools	24-02-2016	28-02-2016
Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 28-02-201		N .			Mixed Signal Design & Verification		
19 ECE 13J41A0463 A JOHN VINEETH III/II Using Cadence Tools 24-02-2016 28-02-2016	18 ECE	13J41A0461	ADI LENIN JAIDEEP	111/11		24-02-2016	28-02-2016
Mixed Signal Design & Verification 24-02-2016 28-02					Mixed Signal Design & Verification		
20 ECE 13J41A0465 B.DAMODHAR III/II Using Cadence Tools 24-02-2016 28-02-2016	19 ECE	13J41A0463	A JOHN VINEETH	111/11		24-02-2016	28-02-2016
Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 28-02-2016					Mixed Signal Design & Verification		
21 ECE 13J41A0472 G CHANDRAKANTH III/II Using Cadence Tools 24-02-2016 28-02-2016 22 ECE 13J41A0474 GAJULAVAMSHI III/II Using Cadence Tools 24-02-2016 28-02-2016 23 ECE 13J41A0475 G.SAI TARUN III/II Using Cadence Tools 24-02-2016 28-02-2016 24 ECE 13J41A0479 K JUNIPER SARDONYX III/II Using Cadence Tools 24-02-2016 28-02-2016 25 ECE 13J41A0480 K MOUNIKA III/II Using Cadence Tools 24-02-2016 28-02-2016 26 ECE 13J41A0482 KADUDULA SAIKIRAN III/II Using Cadence Tools 24-02-2016 28-02-2016 27 ECE 13J41A0485 KOMMUKURI.SABITHA III/II Using Cadence Tools 24-02-2016 28-02-2016 28-02-2016 Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 28-02-2016 28-02-2016 Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 28-02-2016 28-02-2016 Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 28-02-2	20 ECE	13J41A0465	B.DAMODHAR	111/11	<u> </u>	24-02-2016	28-02-2016
Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 28-02-2016					Mixed Signal Design & Verification		
22 ECE 13J41A0474 GAJULAVAMSHI III/II Using Cadence Tools 24-02-2016 28-02-2016 28-02-2016 Mixed Signal Design & Verification 24-02-2016 28-02-2016 28-02-2016 Mixed Signal Design & Verification 24-02-2016 28-02-2016 24 ECE 13J41A0479 K JUNIPER SARDONYX III/II Using Cadence Tools 24-02-2016 28-02-2016 25 ECE 13J41A0480 K MOUNIKA III/II Using Cadence Tools 24-02-2016 28-02-2016 26 ECE 13J41A0482 KADUDULA SAIKIRAN III/II Using Cadence Tools 24-02-2016 28-02-2016 26 ECE 13J41A0482 KADUDULA SAIKIRAN III/II Using Cadence Tools 24-02-2016 28-02-2016 27 ECE 13J41A0485 KOMMUKURI.SABITHA III/II Using Cadence Tools 24-02-2016 28-02-2016 28 ECE 24-02-2016 28-02-2016 28-02-2016 28-02-2016 28 ECE	21 ECE	13J41A0472	G CHANDRAKANTH	111/11	Using Cadence Tools	24-02-2016	28-02-2016
Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 28-02-2016 24-02-2016 28-02-2016 24-02-201					Mixed Signal Design & Verification		
23 ECE 13J41A0475 G.SAI TARUN III/II Using Cadence Tools 24-02-2016 28-02-2016 24 ECE 13J41A0479 K. JUNIPER SARDONYX III/II Using Cadence Tools 24-02-2016 28-02-2016 25 ECE 13J41A0480 K. MOUNIKA III/II Using Cadence Tools 24-02-2016 28-02-2016 26 ECE 13J41A0482 K. ADUDULA SAIKIRAN III/II Using Cadence Tools 24-02-2016 28-02-2016 26 ECE 13J41A0482 K. ADUDULA SAIKIRAN III/II Using Cadence Tools 24-02-2016 28-02-2016 27 ECE 13J41A0485 K. MMUKURI.SABITHA III/II Using Cadence Tools 24-02-2016 28-02-2016 28 ECE 13J41A0485 ECE 13J41A0485 ECE 13J41A0485 ECE EC	22 ECE	13J41A0474	GAJULAVAMSHI	111/11		24-02-2016	28-02-2016
Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 28-02-2016 25 ECE 13J41A0480 K MOUNIKA III/II Using Cadence Tools 24-02-2016 28-02-2016 Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 28-02-2016 Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 28-02-2016 Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 28-02-2016 Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 28-02-2016 Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 28-02-2016 Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 28-02-2016 Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 28-02-2016 Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 28-02-2016 Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 Mixed Signal Design & Verification Using Cadence Tools Mixed Signal Design & Verification Using Cadence Tools Mixed Signal Design & Verification Using Cadence Tools Mixed Signal Design & Verification Mixed Signal Design & Verifi			*				*
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Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 28-02-2016 Mixed Signal Design & Verification Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 Mixed Signal Design & Verification Using Cadence Tools Mixed Signal Design & Verification Mixed Signal Design & Verification Using Cadence Tools Mixed Signal Design & Verification Using Cadence Tools 24-02-2016 28-02-2016 28-02-2016							
25 ECE 13J41A0480 K MOUNIKA III/II Using Cadence Tools 24-02-2016 28-02-2016 26 ECE 13J41A0482 KADUDULA SAIKIRAN III/II Using Cadence Tools 24-02-2016 28-02-2016 27 ECE 13J41A0485 KOMMUKURI.SABITHA III/II Using Cadence Tools 24-02-2016 28-02-2016 28 - 02 - 2016 28 - 02 - 2016 28 - 02 - 2016 29 - 02 - 2016 20 - 2016 20 - 2016 20 - 2016 20 - 20 - 2016 20 - 2016 20 - 2016 20 - 20 - 2016 20 - 2016 20 - 2016 20 - 20 - 2016 20 - 2016 20 - 2016 20 - 20 - 2016 20 - 2016 20 - 2016 20 - 20 - 2016 20 - 2016 20 - 2016 20 - 20 - 2016 20 - 2016 20 - 2016 20 - 20 - 2016 20 - 20 - 2016 20 - 2016 20 - 20 - 2016 20 - 2016	24 ECE	13J41A0479	K JUNIPER SARDONYX	111/11	Using Cadence Tools	24-02-2016	28-02-2016
Mixed Signal Design & Verification 26 ECE 13J41A0482 KADUDULA SAIKIRAN III/II Using Cadence Tools 24-02-2016 28-02-2016 Mixed Signal Design & Verification Mixed Signal Design & Verification Mixed Signal Design & Verification 27 ECE 13J41A0485 KOMMUKURI.SABITHA III/II Using Cadence Tools 24-02-2016 28-02-2016					Mixed Signal Design & Verification		
26 ECE 13J41A0482 KADUDULA SAIKIRAN III/II Using Cadence Tools 24-02-2016 28-02-2016 27 ECE 13J41A0485 KOMMUKURI.SABITHA III/II Using Cadence Tools 24-02-2016 28-02-2016	25 ECE	13J41A0480	K MOUNIKA	111/11		24-02-2016	28-02-2016
Mixed Signal Design & Verification 27 ECE 13J41A0485 KOMMUKURI.SABITHA III/II Using Cadence Tools 24-02-2016 28-02PQI6							
27 ECE 13J41A0485 KOMMUKURI.SABITHA III/II Using Cadence Tools 24-02-2016 28-02 PQ16	26 ECE	13J41A0482	KADUDULA SAIKIRAN	111/11		24-02-2016	28-02-2016
	27 ECE	13J41A0485	KOMMUKURI.SABITHA	111/11	Using Cadence Tools		

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				Mixed Signal Design & Verification		
28 ECE	13J41A0492	MACHNOORI BHAVANI	111/11	Using Cadence Tools	24-02-2016	28-02-2016
				Mixed Signal Design & Verification		
29 ECE	13J41A0495	MOLLI APPANNA	111/11	Using Cadence Tools	24-02-2016	28-02-2016
				Mixed Signal Design & Verification		
30 ECE	13J41A04A3	PILLI SANDEEP	111/11	Using Cadence Tools	24-02-2016	28-02-2016
				Mixed Signal Design & Verification		
31 ECE	13J41A04B2	SANAPALA SAILAJA	111/11	Using Cadence Tools	24-02-2016	28-02-2016
				Mixed Signal Design & Verification		
32 ECE	13J41A04B9	Y.HARIKA REDDY	111/11	Using Cadence Tools	24-02-2016	28-02-2016
				Mixed Signal Design & Verification		
33 ECE	14J45A0418	M.UDAY KIRAN	111/11	Using Cadence Tools	24-02-2016	28-02-2016
				Mixed Signal Design & Verification		
34 ECE	14J45A0420	I.LOKANATH	111/11	Using Cadence Tools	24-02-2016	28-02-2016
				Mixed Signal Design & Verification		
35 ECE	13J41A04D8	GIA ANTONY	111/11	Using Cadence Tools	24-02-2016	28-02-2016
				Mixed Signal Design & Verification		100
36 ECE	13J41A04E3	K SOM SAI	111/11	Using Cadence Tools	24-02-2016	28-02-2016
				Mixed Signal Design & Verification		
37 ECE	13J41A04F3	N.SHARANYA	111/11	Using Cadence Tools	24-02-2016	28-02-2016
				Mixed Signal Design & Verification		
38 ECE	13J41A04F4	NALLA MANIKANTA		Using Cadence Tools	24-02-2016	28-02-2016
£3		2. 32	g-	Mixed Signal Design & Verification		
39 ECE	13J41A04G1	R YAMINI	/	Using Cadence Tools	24-02-2016	28-02-2016
		DICHADILOD		Mixed Signal Design & Verification		
40 ECE	13J41A04G2	RISHABH G R	111/11	Using Cadence Tools	24-02-2016	28-02-2016
				Mixed Signal Design &Verification		,
41 ECE	13J41A04G4	S RAVITEJA	111/11	Using Cadence Tools	24-02 -2016	Red 28-02-2016 Red 28-02-2016 (Autoropadus)
T				Mixed Signal Design & Verification		Reddy Engineering Co (Autogorn Kils) Sammaguda, Dhulapall Kompally), Sec'bad-500
42 ECE	13J41A04G5	S SRI LAKSHMI	111/11	Using Cadence Tools	24-02-2016	amp289202016 C







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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Certificate of Completion

This is to certify that Mrs./Ms./Mr BOYINI RAJASHEKAR bearing Roll Number 13J41A0403 has successfully completed Value added Course in "Mixed Signal Design &

Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2016 to 28.02.2016

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Coordinator



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Certificate of Completion

This is to certify that Mrs./Ms./Mr GOLLA ANURADHA bearing Roll Number 13J41A0416 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2016 to 28.02.2016

Coordinator



HOD-ECE

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PRINC Principal
Malla Reddy Engineering College

Maisammaguda, Dhulapally, (Post Via Kompally), Sec'bad-500 100







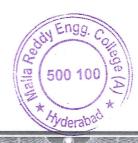
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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING <u>Certificate of Completion</u>

This is to certify that Mrs./Ms./Mr JAJULA SAIRAM bearing Roll Number 13J41A0419 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2016 to 28.02.2016

Coordinator



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Certificate of Completion

This is to certify that Mrs./Ms./Mr K MAHENDRA bearing Roll Number 13J41A0421 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2016 to 28.02.2016

Jal

Coordinator





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PRIMPrincipal
Malla Reddy Engineering College
(Autonomous)

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Certificate of Completion

This is to certify that Mrs./Ms./Mr MOHAMMED AMIR bearing Roll Number 13J41A0433 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2016 to 28.02.2016

Coordinator



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING <u>Certificate of Completion</u>

This is to certify that Mrs./Ms./Mr SRIKANTH.ODETI bearing Roll Number 13J41A0436 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and

Communication Engineering from 24.02.2016 to 28.02.2016

Coordinator



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Certificate of Completion

This is to certify that Mrs./Ms./Mr PADIGELA SAIKIRAN bearing Roll Number 13J41A0437 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2016 to 28.02.2016

Coordinator



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING **Certificate of Completion**

This is to certify that Mrs./Ms./Mr R SAI PRIYANKA bearing Roll Number 13J41A0440 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2016 to 28.02.2016 Rangued

Coordinator





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PRINCIPatincipal Malla Reddy Engineering College

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Certificate of Completion

This is to certify that Mrs./Ms./Mr SHAIK.SHARUF bearing Roll Number 13J41A0446 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2016 to 28.02.2016

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Coordinator



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REPORT ON FIVE DAY WORKSHOP ON "MIXED SIGNAL DESIGN & VERIFICATION USING CADENCE TOOLS"

The department of ECE has organized **two day** workshop on **"Mixed Signal Design &** Verification Using CADENCE Tools" during **24.02.2016 to 28.02.2016** at **VLSI lab** in ECE department from 9.30 A.M to 4.30 P.M.The department has invited the resource person **Mr.N.Madhusudan Reddy**, Senior Design Engineer & his team, **FPGA & ASIC INFOTECH Pvt. Ltd., Hyderabad**

The workshop aims to provide analog and digital IC design using Cadence tool. The Resource person explained about full and semi custom IC design flow and discussed which part of Cadence tool is used in each level of design. He explained analog full custom design and shown practical way of analysis step by step with simulation and layout design.

In the workshop, The participants have an exposure to the Circuit Design & Simulation, Layout, Physical Verification (DRC, LVS), and Extraction. The workshop includes practice sessions on the Cadence design and simulation tools (Encounter, RTL Compiler, Virtuoso, Spectre, Assura and Incisive).

The workshop was mainly focused on III-year B.Tech students. Around 42 students benefitted from this workshop. A good feedback was given by the participants. This Five day workshop was coordinated by Mrs P S Indrani Assoc Prof ECE Dept.





CO-ORDINATOR

HEAD, ECE DEPARTMENT

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