



MALLA REDDY ENGINEERING COLLEGE (AUTONOMOUS)
(An UGC Autonomous Institution, Affiliated to JNTUH, Hyderabad Accredited 2nd time by NAAC
with 'A' Grade & NBA and Recipient of World Bank Assistance under TEQIP--II S.C. 1.1)
Maisammaguda (H), Medchal-Malkajgiri District, Secunderabad,
Telangana State – 500100
www.mrec.ac.in

Department of Electronics and Communication Engineering

CIRCULAR

Date: 12/02/2016

All the students are hereby informed that Value Added Course on “**Mixed Signal Design & Verification Using Cadence Tools**” on 24th to 28th February, 2016 is being organized by the Electronics and Communication Engineering department. The resource person for the course is “**Mr.N.MADHUSUDAN REDDY, FPGA & ASIC INFOTECH PVT LTD, HYDERABAD**”.

Students are advised to register their names to the programme coordinator “**P.S.INDRANI**”, on or before 20th February 2016 and utilize this opportunity to enhance their skills by attending the program.

The detailed schedule of the programme will be displayed in the notice board.


Head of the Department

Copy to:

- 1) Circulation in Students classroom
- 2) All HOD's
- 3) Notice Boards
- 4) PA to principal for filing.


Malla Reddy Engineering College
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Maisammaguda, Dhulapally,
(Post Via Kompally), Sec'bad-500 100



Target Group:

This FDP is open to all the teachers of **Electronics and Communication Engineering** who have interest in learning, teaching and research on the Mixed Signal Design and verification and also Research Scholars and Industry personnel working in Design and Verification, can also register for this programme.

Registration:

Candidates can register by sending the Registration form attached in this brochure along with a **Registration fee of Rs 300** in the form of DD drawn in favor of "Principal, **MREC (Autonomous)**" payable at Hyderabad.

Transportation:

Participants can avail the Institute bus facility from different locations of the twin cities

Important Date:

Last Date for submission of Registration form by post/email: **22nd February 2015.**

Registration form can be downloaded from:

www.mrec.ac.in

Address for Correspondence

P.S.INDRANI

Assoc. Professor, ECE Department

Malla Reddy Engineering College (Autonomous)

Maisammaguda, Dhulapally Post,

Via Kompally, Secunderabad-500100

Email: indu.sri@mrec.ac.in

Mobile: 9490224344

Organizing Committee

Chief Patron:

Sri Ch. Malla Reddy, MP

Founder Chairman, CMR Educational Society

Patrons:

Sri Ch. Mahender Reddy

Secretary, CMR Educational Society

Dr. Ch. Bhadra Reddy

Secretary, MRIMS

Co-Patrons:

Dr. S. Sudhakara Reddy, Principal, MREC (A)

Mr.P.Santhosh Reddy, Director, MREC(A)

Convener:

Dr. M. J. C. Prasad, Professor & Head, ECE Dept.

Co-Ordinators:

P. S. Indrani, Assoc. Professor, ECE Dept.

M. Kavicharan, Assoc. Professor, ECE Dept

Members:

Dr. Kesavan Gopal, Professor, ECE

Mr. P. Ashok Babu, Assoc Professor, ECE

Mrs C. Silpa, Assoc Professor, ECE

Mr.B.Srinivas, Assoc Professor, ECE

Mrs P. Sowjanya, Assoc Professor, ECE

Mrs G.Bharathi Subhashini, Assoc Professor, ECE

Mr.CH.Bhanu Prakash, Assoc Professor, ECE

Mr.T.Srinivas Reddy, Assoc Professor, ECE

Mr.T.Surender Reddy, Assoc Professor, ECE

Mr.V.Srinivas, Asst Professor, ECE

**One week Workshop
on
Mixed Signal Design & Verification Using
Cadence Tools**

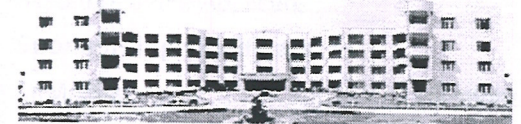
24th to 28th February 2016



**Coordinator
P.S.INDRANI**

Assoc. Professor, ECE Department

**Convener
Dr. M. J. C. PRASAD**
Professor & Head, ECE Department



**Organized by
Department of Electronics and
Communication Engineering (ECE)**

**Malla Reddy Engineering College
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Maisammaguda, Secunderabad – 500 100
Phone: 040-65864982, 040-64634234.

Fax: 040-23090485

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MALLA REDDY ENGINEERING COLLEGE (AUTONOMOUS)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

List of Students Registered for Value Added Course

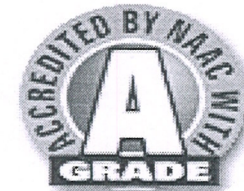
2015-16

S.No	Branch	Roll Number	Student Name	Year/ Sem	Value Added Course	DATE OF INTRODUCTI ON	DATE OF COMPLETION
1	ECE	13J41A0403	BOYINI RAJASHEKAR	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
2	ECE	13J41A0416	GOLLA ANURADHA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
3	ECE	13J41A0419	JAJULA SAIRAM	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
4	ECE	13J41A0421	K MAHENDRA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
5	ECE	13J41A0433	MOHAMMED AMIR	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
6	ECE	13J41A0436	SRIKANTH.ODETI	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
7	ECE	13J41A0437	PADIGELA SAIKIRAN	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
8	ECE	13J41A0440	R SAI PRIYANKA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
9	ECE	13J41A0446	SHAIK.SHARUF	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
10	ECE	13J41A0449	THOUTIREDDYSRINIJA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
11	ECE	13J41A0452	U.PRAMODH KUMAR	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016


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12	ECE	13J41A0454	V BHARANI REDDY	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
13	ECE	13J41A0455	V.L.NKITHA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
14	ECE	13J41A0457	VEERAGONI VENU	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
15	ECE	13J41A0459	Y GANGADHAR	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
16	ECE	13J41A0460	KONDRA RAJU	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
17	ECE	14J45A0411	JADALA THARUN	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
18	ECE	13J41A0461	ADI LENIN JAIDEEP	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
19	ECE	13J41A0463	A JOHN VINEETH	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
20	ECE	13J41A0465	B.DAMODHAR	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
21	ECE	13J41A0472	G CHANDRAKANTH	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
22	ECE	13J41A0474	GAJULAVAMSHI	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
23	ECE	13J41A0475	G.SAI TARUN	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
24	ECE	13J41A0479	K JUNIPER SARDONYX	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
25	ECE	13J41A0480	K MOUNIKA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
26	ECE	13J41A0482	KADUDULA SAIKIRAN	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
27	ECE	13J41A0485	KOMMUKURI.SABITHA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Certificate of Completion

This is to certify that Mrs./Ms./Mr BOYINI RAJASHEKAR bearing Roll Number 13J41A0403 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2016 to 28.02.2016


Coordinator

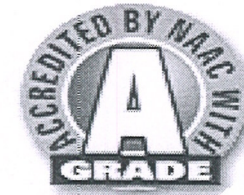



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This is to certify that Mrs./Ms./Mr GOLLA ANURADHA bearing Roll Number 13J41A0416 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2016 to 28.02.2016

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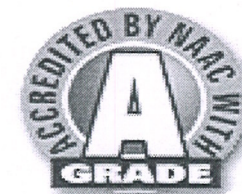
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This is to certify that Mrs./Ms./Mr JAJULA SAIRAM bearing Roll Number 13J41A0419 has successfully completed Value added Course in “Mixed Signal Design & Verification using Cadence Tools” Organized by the Department of Electronics and Communication Engineering from 24.02.2016 to 28.02.2016

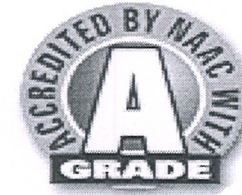
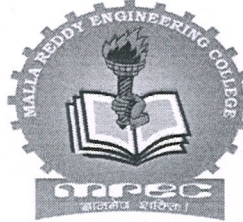

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This is to certify that Mrs./Ms./Mr K MAHENDRA bearing Roll Number 13J41A0421 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2016 to 28.02.2016

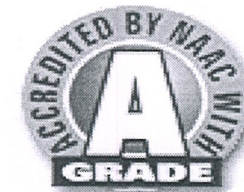
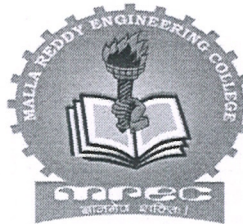
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This is to certify that Mrs./Ms./Mr MOHAMMED AMIR bearing Roll Number 13J41A0433 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2016 to 28.02.2016

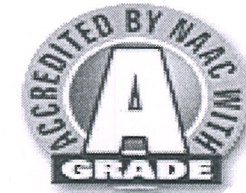
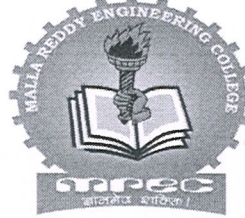
Raveend

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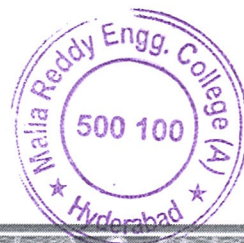
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This is to certify that Mrs./Ms./Mr SRIKANTH.ODETI bearing Roll Number 13J41A0436 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2016 to 28.02.2016

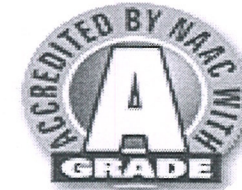

Coordinator




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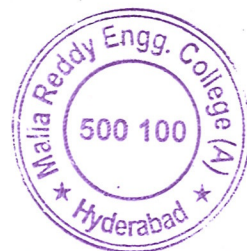
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This is to certify that Mrs./Ms./Mr PADIGELA SAIKIRAN bearing Roll Number 13J41A0437 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2016 to 28.02.2016

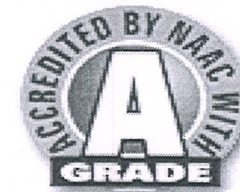
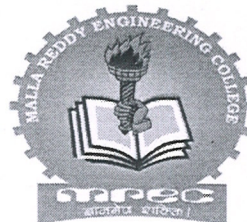

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This is to certify that Mrs./Ms./Mr R SAI PRIYANKA bearing Roll Number 13J41A0440 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2016 to 28.02.2016

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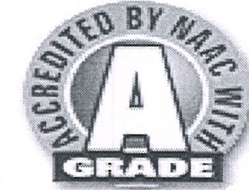
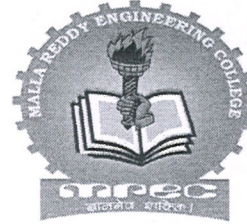
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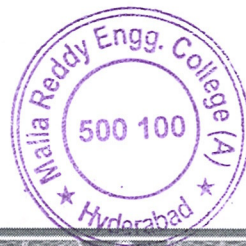
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Certificate of Completion


This is to certify that Mrs./Ms./Mr SHAIK.SHARUF bearing Roll Number 13J41A0446 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2016 to 28.02.2016

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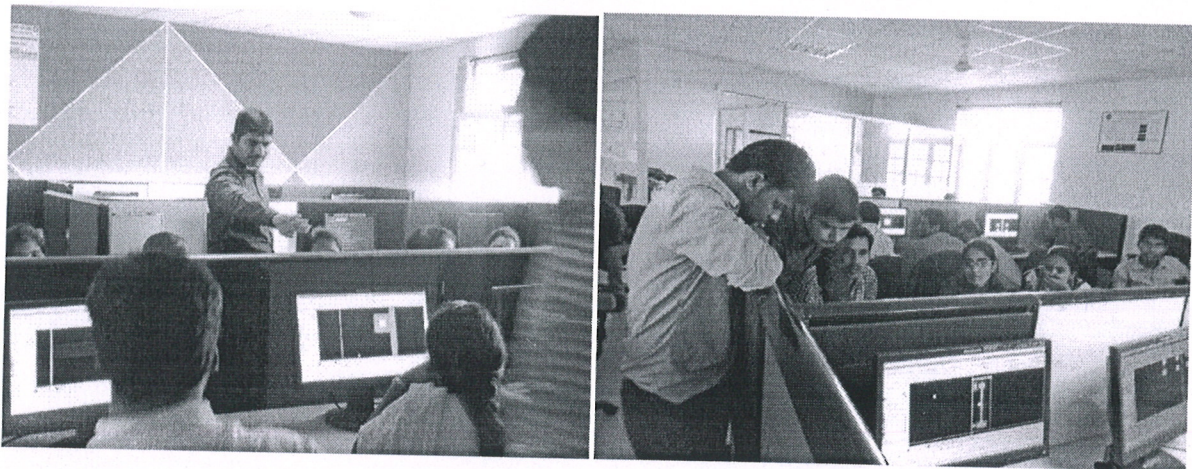
**REPORT ON FIVE DAY WORKSHOP ON
“MIXED SIGNAL DESIGN & VERIFICATION USING CADENCE TOOLS”**

The department of ECE has organized two day workshop on “Mixed Signal Design & Verification Using CADENCE Tools” during 24.02.2016 to 28.02.2016 at VLSI lab in ECE department from 9.30 A.M to 4.30 P.M. The department has invited the resource person **Mr.N.Madhusudan Reddy**, Senior Design Engineer & his team, **FPGA & ASIC INFOTECH Pvt. Ltd., Hyderabad**

The workshop aims to provide analog and digital IC design using Cadence tool. The Resource person explained about full and semi custom IC design flow and discussed which part of Cadence tool is used in each level of design. He explained analog full custom design and shown practical way of analysis step by step with simulation and layout design.

In the workshop, The participants have an exposure to the Circuit Design & Simulation, Layout, Physical Verification (DRC, LVS), and Extraction. The workshop includes practice sessions on the Cadence design and simulation tools (Encounter, RTL Compiler, Virtuoso, Spectre, Assura and Incisive).


The workshop was mainly focused on III-year B.Tech students. Around 42 students benefitted from this workshop. A good feedback was given by the participants. This Five day workshop was coordinated by Mrs P S Indrani Assoc Prof ECE Dept.




CO-ORDINATOR


HEAD, ECE DEPARTMENT




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